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In computer science, specifically software engineering and hardware engineering, formal methods are a particular kind of mathematically based technique for the specification, development and verification of software and hardware systems. The use of formal methods for software and hardware design is motivated by the expectation that, as in other engineering disciplines, performing appropriate ...

### **Formal methods - Wikipedia**

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### **Formal specification - Wikipedia**

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### **9 Formal Reports and Proposals - Pearson**

â€œYouâ€™re writing down a mathematical formula that describes the programâ€™s behavior and using some sort of proof checker thatâ€™s going to check the correctness of that statement,â€• said Bryan Parno, who does research on formal verification and security at Microsoft Research.. The aspiration to create formally verified software has existed nearly as long as the field of computer science.

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### **Methods | Eviction Lab**

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### **AsciiDoc User Guide - methods**

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Acknowledgments Thanks to the following Education Development Center, Inc. (EDC), colleagues for their review of and

### **Distance Education for Teacher Training: Modes, Models**

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## **Graham Hutton: Publications**

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